



AiP74LV4053

Triple 1-of-2 Analog Multiplexer/Demultiplexer

Product Specification

Specification Revision History:

Version	Date	Description
2024-12-A0	2024-12	New
2025-02-A1	2025-02	Modify the parameters



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1、General Description

The AiP74LV4053 is a triple single-pole double-throw (SPDT) analog switch, suitable for use as an analog or digital multiplexer/demultiplexer.

Features:

- Optimized for low-voltage applications: 2.0V to 6.0V
- Accepts TTL input levels between $V_{CC}=2.7V$ and $V_{CC}=3.6V$
- Low ON resistance:
 - 150 Ω (typical) at $V_{CC}-V_{EE}=2.0V$
 - 95 Ω (typical) at $V_{CC}-V_{EE}=3.0V$
 - 75 Ω (typical) at $V_{CC}-V_{EE}=4.5V$
- Logic level translation: to enable 3V logic to communicate with $\pm 3V$ analog signals
- Specified from -40°C to +125°C
- Packaging information: DIP16/SOP16/TSSOP16



Ordering Information:

Tube packing specifications:

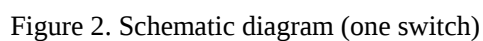
Part number	Packaging form	Marking code	Tube quantity	Boxed tube quantity	Boxed quantity	Notes
AiP74LV4053DA16.TB	DIP16	74LV4053	25 PCS/tube	40 tube/box	1000 PCS/box	Dimensions of plastic enclosure: 19.0mm×6.4mm Pin spacing: 2.54mm

Reel packing specifications:

Part number	Packaging form	Marking code	Reel quantity	Boxed reel quantity	Notes
AiP74LV4053SA16.TR	SOP16	74LV4053	4000 PCS/reel	8000 PCS/box	Dimensions of plastic enclosure: 10.0mm×3.9mm Pin spacing:1.27mm
AiP74LV4053TA16.TR	TSSOP16	74LV4053	5000 PCS/reel	10000 PCS/box	Dimensions of plastic enclosure: 5.0mm×4.4mm Pin spacing:0.65mm

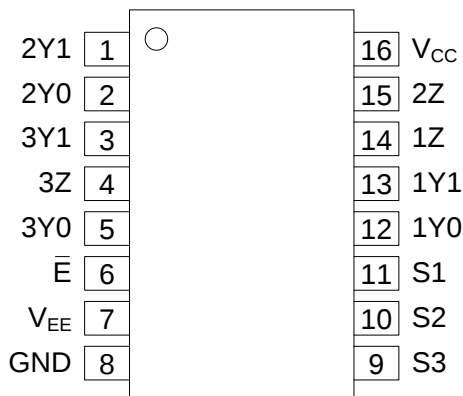
Note: If the physical information is inconsistent with the ordering information, please refer to the actual product.

2.1、 Block Diagram





2.2、Pin Configurations



2.3、Pin Description

Pin No.	Pin Name	Description
1	2Y1	independent input or output
2	2Y0	independent input or output
3	3Y1	independent input or output
4	3Z	common output or input
5	3Y0	independent input or output
6	$\bar{E}$	enable input (active LOW)
7	V_{EE}	supply voltage
8	GND	ground supply voltage
9	S3	select input
10	S2	select input
11	S1	select input
12	1Y0	independent input or output
13	1Y1	independent input or output
14	1Z	common output or input
15	2Z	common output or input
16	V_{CC}	supply voltage

2.4、Function Table

Inputs		Channel ON
$\bar{E}$	S_n	
L	L	nY0 to nZ
L	H	nY1 to nZ
H	X	switches off

Note: H=HIGH voltage level; L=LOW voltage level; X=don't care.



3、Electrical Parameter

3.1、Absolute Maximum Ratings

(Voltages are referenced to GND (ground=0V), unless otherwise specified.)

Parameter	Symbol	Conditions		Min.	Max.	Unit
supply voltage	V _{CC}	-		-0.5	+7.0	V
input clamping current	I _{IK}	V _I < -0.5V or V _I >V _{CC} +0.5V		-	±20	mA
switch clamping current	I _{SK}	V _{SW} < -0.5V or V _{SW} >V _{CC} +0.5V		-	±20	mA
switch current	I _{SW}	V _{SW} > -0.5V or V _{SW} <V _{CC} +0.5V; source or sink current		-	±25	mA
storage temperature	T _{stg}	-		-65	+150	°C
total power dissipation	P _{tot}	-		-	500	mW
soldering temperature	T _L	10s	DIP	245		°C
			SOP/TSSOP	260		

3.2、Recommended Operating Conditions

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
supply voltage	V_{CC}	-	2	3.3	6	V
input voltage	V_I	-	0	-	V_{CC}	V
switch voltage	V_{SW}	-	0	-	V_{CC}	V
ambient temperature	T_{amb}	in free air	-40	-	+125	°C
input transition rise and fall rate	$\Delta t/\Delta V$	$V_{CC}=2.0V$ to $2.7V$	-	-	200	ns/V
		$V_{CC}=2.7V$ to $6.0V$	-	-	100	ns/V

3.3、Electrical Characteristics

3.3.1、DC Characteristics 1

($T_{amb}=-40^{\circ}C$ to $+85^{\circ}C$, voltages are referenced to GND (ground=0V), unless otherwise specified.)

Parameter	Symbol	Conditions (V)	Min.	Typ.	Max.	Unit
HIGH-level input voltage	V_{IH}	$V_{CC}=2.0V$	1.4	-	-	V
		$V_{CC}=2.7V$ to $3.6V$	2.0	-	-	V
		$V_{CC}=4.5V$	3.15	-	-	V
		$V_{CC}=6.0V$	4.20	-	-	V
LOW-level input voltage	V_{IL}	$V_{CC}=2.0V$	-	-	0.5	V
		$V_{CC}=2.7V$ to $3.6V$	-	-	0.7	V
		$V_{CC}=4.5V$	-	-	1.15	V
		$V_{CC}=6.0V$	-	-	1.60	V
input leakage current	I_I	$V_I=V_{CC}$ or GND	$V_{CC}=3.6V$	-	-	1.0
			$V_{CC}=6.0V$	-	-	2.0
OFF-state leakage current	$I_{S(OFF)}$	$V_I=V_{IH}$ or V_{IL} ; see Figure 12	$V_{CC}=3.6V$	-	-	1.0
			$V_{CC}=6.0V$	-	-	2.0
ON-state leakage current	$I_{S(ON)}$	$V_I=V_{IH}$ or V_{IL} ; see Figure 13	$V_{CC}=3.6V$	-	-	1.0
			$V_{CC}=6.0V$	-	-	2.0
supply current	I_{CC}	$V_I=V_{CC}$ or GND; $I_O=0A$	$V_{CC}=3.6V$	-	-	20
			$V_{CC}=6.0V$	-	-	40
additional supply	ΔI_{CC}	per input; $V_I=V_{CC}-0.6V$;	-	-	500	uA



current		$V_{CC}=2.7V$ to $3.6V$					
ON resistance (peak)	$R_{ON(peak)}$	$V_I=0V$ to $V_{CC}-V_{EE}$	$V_{CC}=2.0V$; $I_{SW}=1000uA$	-	290	650	Ω
			$V_{CC}=2.7V$; $I_{SW}=1000uA$	-	150	300	Ω
			$V_{CC}=3.0V$ to $3.6V$; $I_{SW}=1000uA$	-	100	200	Ω
			$V_{CC}=4.5V$; $I_{SW}=1000uA$	-	80	160	Ω
			$V_{CC}=6.0V$; $I_{SW}=1000uA$	-	65	130	Ω
ON resistance mismatch between channels	ΔR_{ON}	$V_I=0V$ to $V_{CC}-V_{EE}$	$V_{CC}=2.0V$; $I_{SW}=1000uA$	-	5	-	Ω
			$V_{CC}=2.7V$; $I_{SW}=1000uA$	-	4	-	Ω
			$V_{CC}=3.0V$ to $3.6V$; $I_{SW}=1000uA$	-	4	-	Ω
			$V_{CC}=4.5V$; $I_{SW}=1000uA$	-	3	-	Ω
			$V_{CC}=6.0V$; $I_{SW}=1000uA$	-	2	-	Ω
ON resistance (rail)	$R_{ON(rail)}$	$V_I=GND$	$V_{CC}=2.0V$; $I_{SW}=1000uA$	-	110	235	Ω
			$V_{CC}=2.7V$; $I_{SW}=1000uA$	-	85	170	Ω
			$V_{CC}=3.0V$ to $3.6V$; $I_{SW}=1000uA$	-	65	130	Ω
			$V_{CC}=4.5V$; $I_{SW}=1000uA$	-	60	120	Ω
			$V_{CC}=6.0V$; $I_{SW}=1000uA$	-	50	100	Ω
		$V_I=V_{CC}-V_{EE}$	$V_{CC}=2.0V$; $I_{SW}=1000uA$	-	150	320	Ω
			$V_{CC}=2.7V$; $I_{SW}=1000uA$	-	110	220	Ω
			$V_{CC}=3.0V$ to $3.6V$; $I_{SW}=1000uA$	-	85	170	Ω
			$V_{CC}=4.5V$; $I_{SW}=1000uA$	-	70	140	Ω
			$V_{CC}=6.0V$; $I_{SW}=1000uA$	-	60	120	Ω

Note:

[1] Typical values are measured at $T_{amb}=25^{\circ}C$.



3.3.2、DC Characteristics 2

($T_{amb} = -40^{\circ}\text{C}$ to $+125^{\circ}\text{C}$, voltages are referenced to GND (ground=0V), unless otherwise specified.)

Parameter	Symbol	Conditions (V)		Min.	Typ.	Max.	Unit
HIGH-level input voltage	V_{IH}	$V_{CC}=2.0\text{V}$		1.4	-	-	V
		$V_{CC}=2.7\text{V to }3.6\text{V}$		2.0	-	-	V
		$V_{CC}=4.5\text{V}$		3.15	-	-	V
		$V_{CC}=6.0\text{V}$		4.20	-	-	V
LOW-level input voltage	V_{IL}	$V_{CC}=2.0\text{V}$		-	-	0.5	V
		$V_{CC}=2.7\text{V to }3.6\text{V}$		-	-	0.7	V
		$V_{CC}=4.5\text{V}$		-	-	1.15	V
		$V_{CC}=6.0\text{V}$		-	-	1.60	V
input leakage current	I_I	$V_I = V_{CC}$ or GND	$V_{CC}=3.6\text{V}$	-	-	2.0	μA
			$V_{CC}=6.0\text{V}$	-	-	4.0	μA
OFF-state leakage current	$I_{S(OFF)}$	$V_I = V_{IH}$ or V_{IL} ; see Figure 12	$V_{CC}=3.6\text{V}$	-	-	2.0	μA
			$V_{CC}=6.0\text{V}$	-	-	4.0	μA
ON-state leakage current	$I_{S(ON)}$	$V_I = V_{IH}$ or V_{IL} ; see Figure 13	$V_{CC}=3.6\text{V}$	-	-	2.0	μA
			$V_{CC}=6.0\text{V}$	-	-	4.0	μA
supply current	I_{CC}	$V_I = V_{CC}$ or GND; $I_O = 0\text{A}$	$V_{CC}=3.6\text{V}$	-	-	40	μA
			$V_{CC}=6.0\text{V}$	-	-	80	μA
additional supply current	ΔI_{CC}	per input; $V_I = V_{CC} - 0.6\text{V}$; $V_{CC} = 2.7\text{V to }3.6\text{V}$		-	-	850	μA
ON resistance (peak)	$R_{ON(peak)}$	$V_I = 0\text{V to }V_{CC} - V_{EE}$	$V_{CC}=2.0\text{V}$; $I_{SW}=1000\mu\text{A}$	-	-	750	Ω
			$V_{CC}=2.7\text{V}$; $I_{SW}=1000\mu\text{A}$	-	-	350	Ω
			$V_{CC}=3.0\text{V to }3.6\text{V}$; $I_{SW}=1000\mu\text{A}$	-	-	250	Ω
			$V_{CC}=4.5\text{V}$; $I_{SW}=1000\mu\text{A}$	-	-	200	Ω
			$V_{CC}=6.0\text{V}$; $I_{SW}=1000\mu\text{A}$	-	-	150	Ω
ON resistance (rail)	$R_{ON(rail)}$	$V_I = \text{GND}$	$V_{CC}=2.0\text{V}$; $I_{SW}=1000\mu\text{A}$	-	-	270	Ω
			$V_{CC}=2.7\text{V}$; $I_{SW}=1000\mu\text{A}$	-	-	200	Ω
			$V_{CC}=3.0\text{V to }3.6\text{V}$; $I_{SW}=1000\mu\text{A}$	-	-	150	Ω
			$V_{CC}=4.5\text{V}$; $I_{SW}=1000\mu\text{A}$	-	-	140	Ω
			$V_{CC}=6.0\text{V}$; $I_{SW}=1000\mu\text{A}$	-	-	120	Ω
		$V_I = V_{CC} - V_{EE}$	$V_{CC}=2.0\text{V}$; $I_{SW}=1000\mu\text{A}$	-	-	370	Ω
			$V_{CC}=2.7\text{V}$;	-	-	250	Ω



			I _{SW} =1000uA				
			V _{CC} =3.0V to 3.6V; I _{SW} =1000uA	-	-	205	Ω
			V _{CC} =4.5V; I _{SW} =1000uA	-	-	150	Ω
			V _{CC} =6.0V; I _{SW} =1000uA	-	-	135	Ω

3.3.3、AC Characteristics 1

(T_{amb}=-40℃ to +85℃, voltages are referenced to GND (ground=0V), unless otherwise specified.)

Parameter	Symbol	Conditions		Min.	Typ.	Max.	Unit
propagation delay	t _{pd}	nYn to nZ, nZ to nYn; see Figure 4	V _{CC} =2.0V	-	9	17	ns
			V _{CC} =2.7V	-	6	13	ns
			V _{CC} =3.0V to 3.6V	-	5	10	ns
			V _{CC} =4.5V	-	4	9	ns
			V _{CC} =6.0V	-	3	7	ns
enable time	t _{en}	E̅ to nYn, nZ; see Figure 5	V _{CC} =2.0V	-	73	90	ns
			V _{CC} =2.7V	-	43	52	ns
			V _{CC} =3.0V to 3.6V; C _L =15pF	-	32	-	ns
			V _{CC} =3.0V to 3.6V	-	32	39	ns
			V _{CC} =4.5V	-	22	27	ns
			V _{CC} =6.0V	-	17	21	ns
		Sn to Yn, see Figure 5	V _{CC} =2.0V	-	80	96	ns
			V _{CC} =2.7V	-	47	57	ns
			V _{CC} =3.0V to 3.6V; C _L =15pF	-	35	-	ns
			V _{CC} =3.0V to 3.6V	-	35	42	ns
			V _{CC} =4.5V	-	25	30	ns
			V _{CC} =6.0V	-	19	23	ns
disable time	t _{dis}	E̅ to nYn; see Figure 5	V _{CC} =2.0V	-	24	29	ns
			V _{CC} =2.7V	-	18	21	ns
			V _{CC} =3.0V to 3.6V; C _L =15pF	-	14	-	ns
			V _{CC} =3.0V to 3.6V	-	14	17	ns
			V _{CC} =4.5V	-	11	13	ns
			V _{CC} =6.0V	-	10	12	ns
		Sn to Yn; see Figure 5	V _{CC} =2.0V	-	24	29	ns
			V _{CC} =2.7V	-	17	20	ns
			V _{CC} =3.0V to 3.6V; C _L =15pF	-	13	-	ns
			V _{CC} =3.0V to 3.6V	-	13	16	ns
			V _{CC} =4.5V	-	11	13	ns
			V _{CC} =6.0V	-	9	11	ns

Note:

[1] Typical values are measured at T_{amb}=25℃.



[2] t_{pd} is the same as t_{PLH} and t_{PHL} .

t_{en} is the same as t_{PZL} and t_{PZH} .

t_{dis} is the same as t_{PLZ} and t_{PHZ} .

[3] Typical values are measured at nominal supply voltage ($V_{CC}=3.3V$).

3.3.4、AC Characteristics 2

($T_{amb}=-40^{\circ}C$ to $+125^{\circ}C$, voltages are referenced to GND (ground=0V), unless otherwise specified.)

Parameter	Symbol	Conditions		Min.	Typ.	Max.	Unit
propagation delay	t_{pd}	nYn to nZ, nZ to nYn; see Figure 4	$V_{CC}=2.0V$	-	-	20	ns
			$V_{CC}=2.7V$	-	-	15	ns
			$V_{CC}=3.0V$ to 3.6V	-	-	12	ns
			$V_{CC}=4.5V$	-	-	10	ns
			$V_{CC}=6.0V$	-	-	8	ns
enable time	t_{en}	$\overline{E}$ to nYn, nZ; see Figure 5	$V_{CC}=2.0V$	-	-	104	ns
			$V_{CC}=2.7V$	-	-	61	ns
			$V_{CC}=3.0V$ to 3.6V	-	-	45	ns
			$V_{CC}=4.5V$	-	-	31	ns
			$V_{CC}=6.0V$	-	-	24	ns
		Sn to Yn; see Figure 5	$V_{CC}=2.0V$	-	-	112	ns
			$V_{CC}=2.7V$	-	-	66	ns
			$V_{CC}=3.0V$ to 3.6V	-	-	49	ns
			$V_{CC}=4.5V$	-	-	35	ns
			$V_{CC}=6.0V$	-	-	27	ns
disable time	t_{dis}	$\overline{E}$ to nYn, nZ; see Figure 5	$V_{CC}=2.0V$	-	-	34	ns
			$V_{CC}=2.7V$	-	-	25	ns
			$V_{CC}=3.0V$ to 3.6V	-	-	20	ns
			$V_{CC}=4.5V$	-	-	16	ns
			$V_{CC}=6.0V$	-	-	13	ns
		Sn to Yn; see Figure 5	$V_{CC}=2.0V$	-	-	33	ns
			$V_{CC}=2.7V$	-	-	24	ns
			$V_{CC}=3.0V$ to 3.6V	-	-	18	ns
			$V_{CC}=4.5V$	-	-	15	ns
			$V_{CC}=6.0V$	-	-	13	ns

Note:

[1] t_{pd} is the same as t_{PLH} and t_{PHL} .

t_{en} is the same as t_{PZL} and t_{PZH} .

t_{dis} is the same as t_{PLZ} and t_{PHZ} .



3.3.5、AC Characteristics 3

($T_{amb}=25^{\circ}\text{C}$, $GND=0V$, $t_r=t_f\leq 6.0\text{ns}$, $V_I=GND$ or V_{CC} , unless otherwise specified.)

Parameter	Symbol	Conditions		Min.	Typ.	Max.	Unit
total harmonic distortion	THD	f_i =1kHz; C_L =50pF; R_L =10k Ω ; see Figure 8	V_{CC} =3.0V; V_I =2.75V(p-p)	-	0.8	-	%
			V_{CC} =6.0V; V_I =5.5V(p-p)	-	0.4	-	%
		f_i =10kHz; C_L =50pF; R_L =10k Ω ; see Figure 8	V_{CC} =3.0V; V_I =2.75V(p-p)	-	2.4	-	%
			V_{CC} =6.0V; V_I =5.5V(p-p)	-	1.2	-	%
-3dB frequency response	$f_{(-3dB)}$	C_L =50pF; R_L =50 Ω ; see Figure 6	V_{CC} =3.0V	-	180	-	MHz
			V_{CC} =6.0V	-	200	-	MHz
isolation (OFF-state)	α_{iso}	f_i =1MHz; C_L =50pF; R_L =600 Ω ; see Figure 7	V_{CC} =3.0V	-	-50	-	dB
			V_{CC} =6.0V	-	-50	-	dB
crosstalk voltage	V_{ct}	between digital inputs and switch; f_i =1MHz; C_L =50pF; R_L =600 Ω ; see Figure 9	V_{CC} =3.0V	-	0.11	-	V
			V_{CC} =6.0V	-	0.12	-	V
crosstalk	Xtalk	f_i =1MHz; C_L =50pF; R_L =600 Ω ; see Figure 10	V_{CC} =3.0V	-	-60	-	dB
			V_{CC} =6.0V	-	-60	-	dB

Note:

[1] To obtain 0dBm level at output for 1MHz (0dBm=1mW into 50 Ω), adjust f_i voltage.

[2] To obtain 0dBm level at output for 1MHz (0dBm=1mW into 600 Ω), adjust f_i voltage.



4、Testing Circuit

4.1、AC Testing Circuit 1

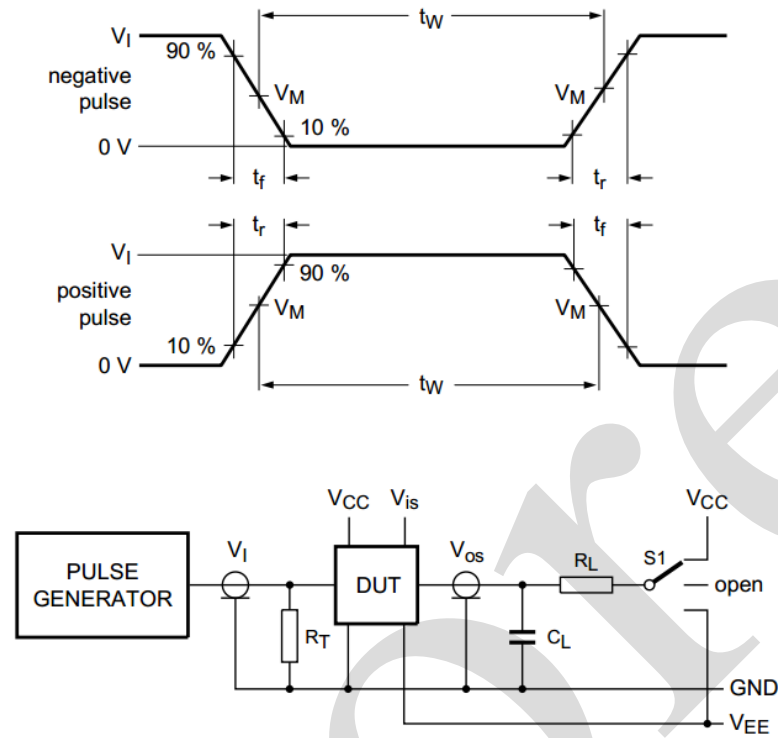


Figure 3. Test circuit for switching times

Definitions for test circuit:

R_T = termination resistance should be equal to the output impedance Z_O of the pulse generator.

C_L = load capacitance including jig and probe capacitance.

R_L = load resistance.

$S1$ = Test selection switch.

4.2、AC Testing Waveforms

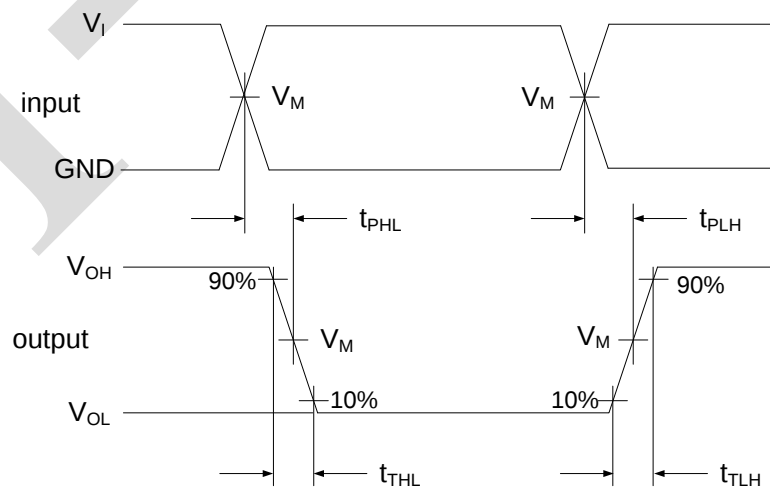


Figure 4. nYn to nZ, nZ to nYn propagation delays

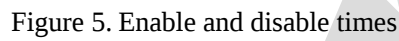


Figure 7. Test circuit for measuring isolation (OFF-state)

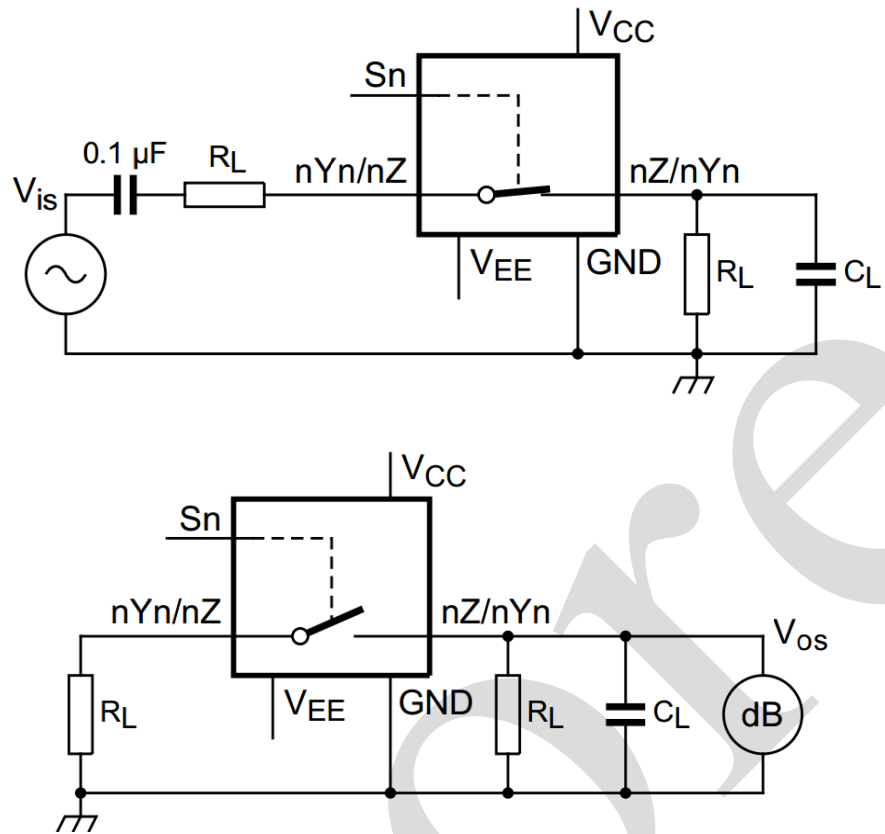


Figure 8. Test circuits for measuring crosstalk between any two switches/multiplexers

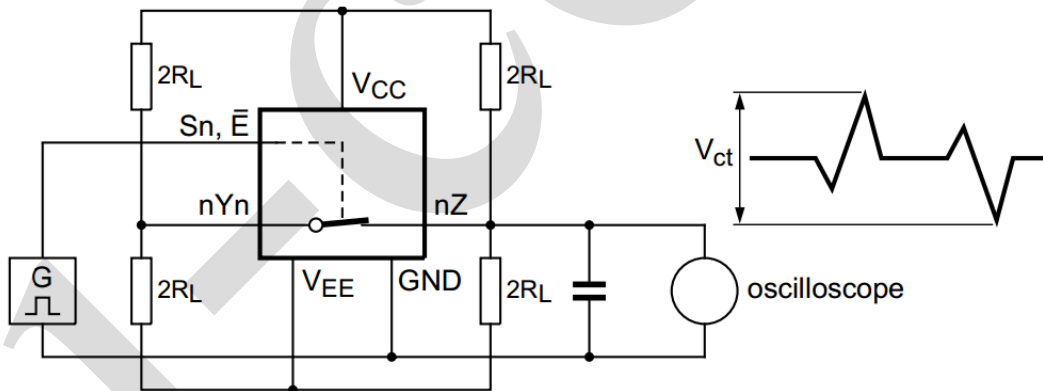
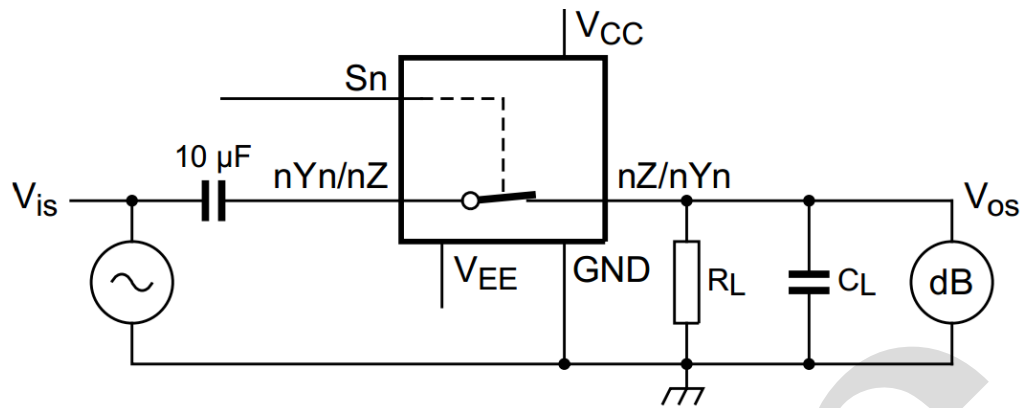


Figure 9. Test circuit for measuring crosstalk between control input and any switch



$$V_{CC} = 4.5 \text{ V}; \text{GND} = 0 \text{ V}; V_{EE} = -4.5 \text{ V}; R_L = 50 \Omega; R_S = 1 \text{ k}\Omega$$

Figure 10. Test circuit for frequency response

4.4、On Resistance Test Circuit

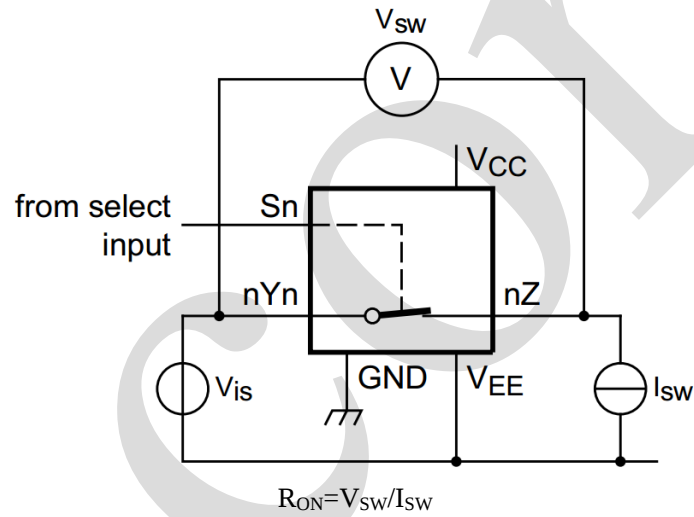


Figure 11. Test circuit for measuring R_{ON}

4.5、DC Testing Circuit

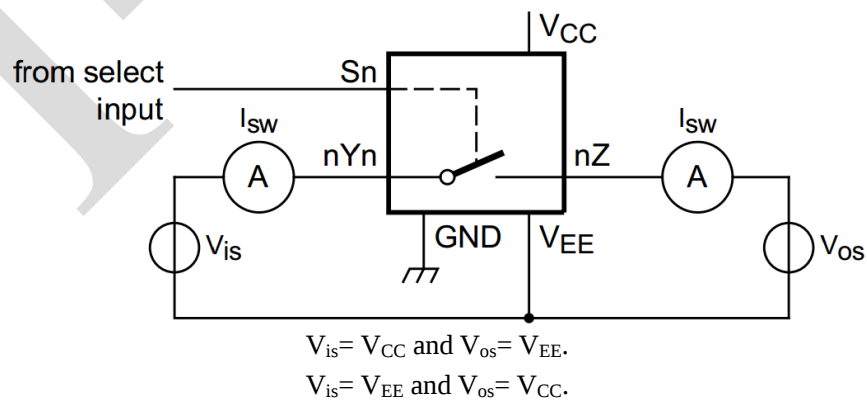
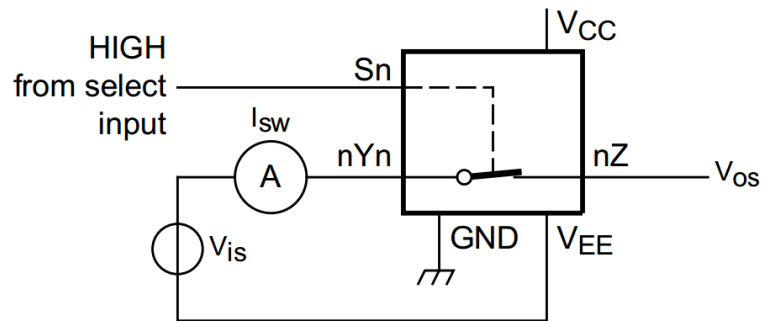


Figure 12. Test circuit for measuring OFF-state leakage current



$V_{is} = V_{CC}$ and $V_{os} = \text{open-circuit}$.

$V_{is} = V_{EE}$ and $V_{os} = \text{open-circuit}$.

Figure 13. Test circuit for measuring ON-state leakage current

4.6、Measurement Points

Supply voltage	Input	Output		
V_{CC}	V_M	V_M	V_X	V_Y
$<2.7V$	$0.5 \times V_{CC}$	$0.5 \times V_{CC}$	$V_{EE} + 0.1V_{CC}$	$V_o - 0.1V_{CC}$
$2.7V$ to $3.6V$	$1.5V$	$1.5V$	$V_{EE} + 0.3V$	$V_o - 0.3V$
$>3.6V$	$0.5 \times V_{CC}$	$0.5 \times V_{CC}$	$V_{EE} + 0.1V_{CC}$	$V_o - 0.1V_{CC}$

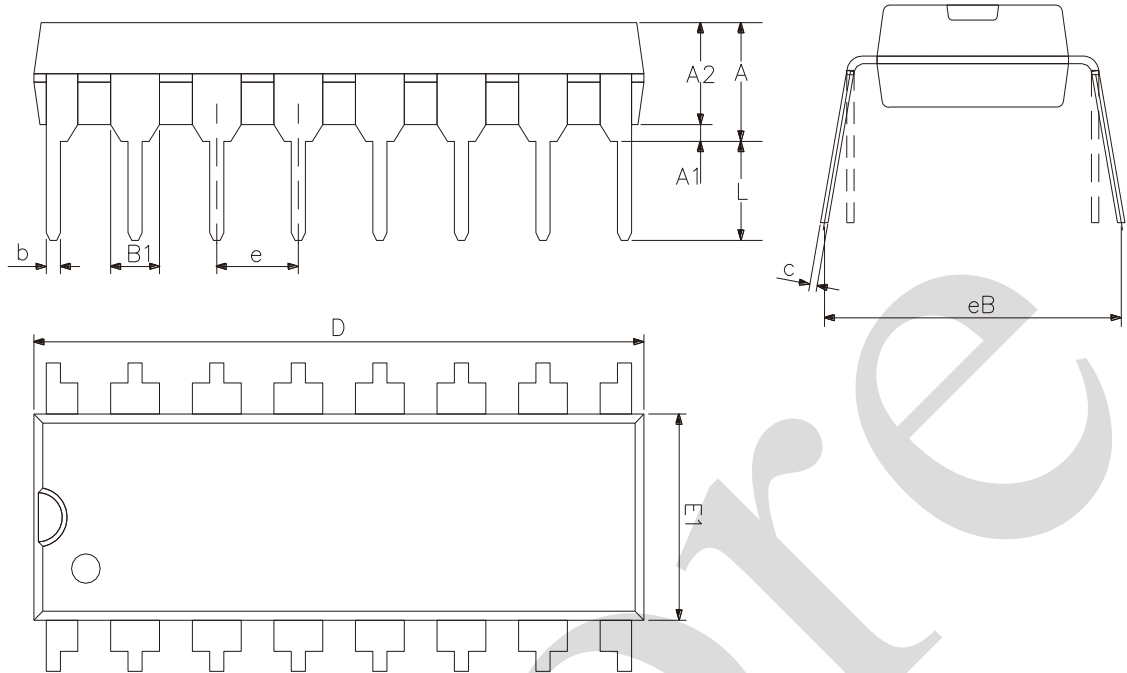
4.7、Test Data

Supply voltage	Input		Load		V_{EXT}		
V_{CC}	V_I	t_r, t_f	C_L	R_L	t_{PHL}, t_{PLH}	t_{pZH}, t_{pHZ}	t_{pZL}, t_{pLZ}
$<2.7V$	V_{CC}	$\leq 6ns$	$50pF$	$1k\Omega$	open	V_{EE}	$2 \times V_{CC}$
$2.7V$ to $3.6V$	$2.7V$	$\leq 6ns$	$15 pF, 50pF$	$1k\Omega$	open	V_{EE}	$2 \times V_{CC}$
$>3.6V$	V_{CC}	$\leq 6ns$	$50pF$	$1k\Omega$	open	V_{EE}	$2 \times V_{CC}$



5、Package Information

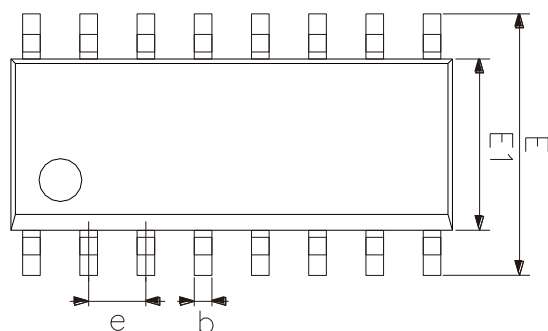
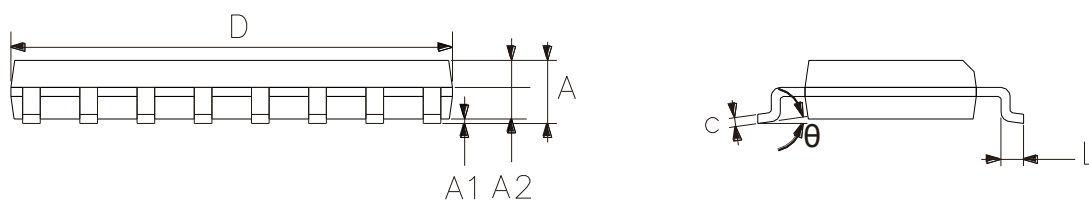
5.1、DIP16



2023/12/A	Dimensions In Millimeters	
Symbol	Min	Max
A2	3.00	3.60
A1	0.51	—
A	3.60	5.33
L	3.00	3.60
b	0.36	0.56
B1	1.52	
D	18.80	19.94
E1	6.20	6.60
e	2.54	
c	0.20	0.36
eB	7.62	9.30



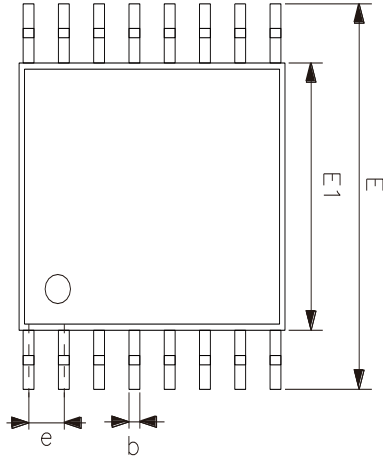
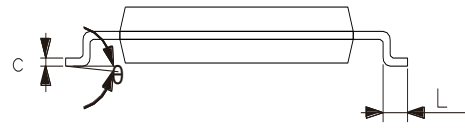
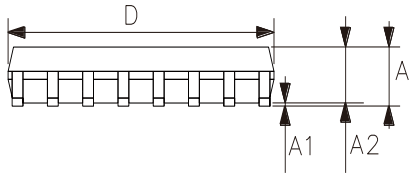
5.2、SOP16



2023/12/A	Dimensions In Millimeters	
Symbol	Min.	Max.
A	1.35	1.80
A1	0.10	0.25
A2	1.25	1.55
b	0.33	0.51
c	0.19	0.25
D	9.50	10.10
E	5.80	6.30
E1	3.70	4.10
e	1.27	
L	0.35	0.89
θ	0°	8°



5.3、TSSOP16



2023/12/A	Dimensions In Millimeters	
Symbol	Min	Max
A	—	1.20
A1	0.05	0.15
A2	0.80	1.05
b	0.19	0.30
c	0.09	0.20
D	4.90	5.10
E1	4.30	4.50
E	6.20	6.60
e	0.65	
L	0.45	0.75
θ	0°	8°



6、Statements And Notes

6.1、The name and content of Hazardous substances or Elements in the product

Part name	Hazardous substances or Elements									
	Lead and lead compounds	Mercury and mercury compounds	Cadmium and cadmium compounds	Hexavalent chromium compounds	Polybrominated biphenyls	Polybrominated biphenyl ethers	Dibutyl phthalate	Butylbenzyl phthalate	Di-2-ethylhexyl phthalate	Diisobutyl phthalate
Lead frame	○	○	○	○	○	○	○	○	○	○
Plastic resin	○	○	○	○	○	○	○	○	○	○
Chip	○	○	○	○	○	○	○	○	○	○
The lead	○	○	○	○	○	○	○	○	○	○
Plastic sheet installed	○	○	○	○	○	○	○	○	○	○
explanation	○: Indicates that the content of hazardous substances or elements in the detection limit of the following the SJ/T11363-2006 standard. ×: Indicates that the content of hazardous substances or elements exceeding the SJ/T11363-2006 Standard limit requirements.									

6.2、Notes

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